



MALINENI LAKSHMAIAH WOMEN'S ENGINEERING COLLEGE (AUTONOMOUS)

(Accredited by "NBA" & "NAAC" with A+ Grade | Approved by AICTE, New Delhi & Permanently Affiliated to JNTUK, Kakinada)
Pulladigunta (Vil), Vatticherukuru (Md), Prathipadu Road, Guntur – 522 017 A.P.



Department of Electronics Engineering (VLSI Design & Tech.):2026-27

S. No.	Degree	Department	Specialization	Name of the Faculty	Designation	Qualification	Date of Joining	Whether Appointed by Selection Committee (Yes/No)	Aadhar Number	PAN No.	Total Exp.
1	M.Tech	EE VLSI	VLSI	K.Nalini	Asst. Professor	M.Tech	13-06-2017	Yes	506043387457	AFUPN7658G	20
2	M.Tech	EE VLSI	VLSI	P.Vijay Babu	Asst. Professor	M.Tech	12-07-2022	Yes	884919125250	DFSPP8797F	5
3	M.Tech	EE VLSI	VLSI & ES	D. Meena	Asst. Professor	M.Tech	23-06-2025	Yes	374856011757	BKGPD2505Q	4
4	M.Tech	EE VLSI	VLSI & ES	P. Sindhu	Asst. Professor	M.Tech	02-07-2025	Yes	718044795595	GPLPP0043E	2
5	M.Tech	EE VLSI	VLSI	B.Manasa	Asst. Professor	M.Tech	13-07-2022	Yes	605870152248	CZAPB6431P	4
6	M.Tech	EE VLSI	VLSI	T. Srilakshmi	Asst. Professor	M.Tech	10-06-2024	Yes	719377754252	FWBP0891N	2

Head of the Department

Principal